
The Bachelor (1999) DVDRip MKV 400MB

Pornograf (2011) DVDRip MKV 450MB - Ismene - The Best of Straight-to-DVD-Movie Titles American Wedding [2003] DVDRip 9GB. 1. 1. Field of the Invention The present invention relates to a thin film transistor (TFT), and more particularly, to a method for forming a high resolution and high performance TFT with reduced manufacturing cost. 2. Discussion of the Related Art Generally, a TFT used for an active matrix type liquid crystal display (LCD) is formed by either an amorphous silicon (a-Si) process or a low temperature polycrystalline silicon (LTPS) process. The a-Si process was developed by and obtained from the Eastman Kodak Company, but is generally difficult to be used in a high resolution larger display panel. On the other hand, a LTPS TFT has low cost equipment and is flexible, and therefore is very likely to be a main stream of TFTs in the future. However, there are many problems regarding the LTPS TFT. The LTPS TFT is obtained by depositing an amorphous silicon layer and then by crystallizing the amorphous silicon layer using a laser or heat. The crystallizing rate of the amorphous silicon layer directly depends on the kind of TFT and the laser used, and the crystallizing method is a process critical in LTPS TFT fabrication. The process for crystallizing the amorphous silicon layer is repeated several hundreds to several thousands of times in an active layer of an LCD. The crystallization process is also repeated several times in each pixel region. Therefore, much time is required for the crystallization process. Also, a polycrystalline silicon layer fabricated by the crystallizing method is polycrystallized randomly. This causes an irregular structure. Meanwhile, when the amorphous silicon layer is crystallized by the laser, the fabricated polycrystalline silicon layer is distorted, which is referred to as a frame problem. The frame problem is a phenomenon where the width of a channel region of a TFT in an LCD is wider than that of a gate electrode which is simultaneously formed. In order to reduce the frame problem, an etching rate of a first photoresist layer having a shielding function during the patterning process of a gate insulating layer must be decreased. This, however, causes the loss of the



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